FIVE DAYS VIRTUAL FACULTYDEVELOPMENT PROGRAMME (**FDP**)
(Online Short-Term Course)
Python programming for Artificial Intelligence (AI), High-Performance Computing (HPC), and Data Analytics
20th June - 30th June 2022
Organized by
E & ICT Academy, National Institute of Technology, Warangal
In association with
Dept. of ECE, JSS Academy of Technical Education, Bangalore
(**Sponsored by Ministry of Electronics and Information Technology(MeitY), Govt. of India**)

Preamble

Electronics & ICT Academy" was set up at NIT Warangal with financial assistance from MeitY, GoI. The jurisdiction of this academy is Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry and Andaman & Nicobar Islands. The academy role is to offer faculty development programme in standardized courses and emerging areas of Electronics, Information Communication Technologies, curriculum development at par with industries requirement as well as training & consultancy services for Industry, CEP for working professionals, Advice and support for technical incubation and entrepreneurial activities.

About the FDP

The FDP is on Python programming for Artificial Intelligence (AI), High-Performance Computing (HPC), and Data Analytics. The Artificial Intelligence (AI) and High-Performance Computing (HPC) Program is designed for all engineering disciplines. In a world fueled by humongous amount of data, processes like Data Analytics, Artificial Intelligence and HPC are almost indispensable. This niche program particularly aims to impart these quintessential skills to the participants. This is done by using latest tools and platforms to visualize the power of AI & HPC, and how it can be used to speed up processes.

Major Course Contents

- ❖ Python for data science
- ❖ Forming Explorative Data Analytics with Python Packages and libraries
- ❖ Introduction to data analytics and Python fundamentals
- ❖ Building deep learning applications
- ❖ Understanding and developing deep learning application using popular deep learning algorithms
- ❖ Deep learning for computer vision applications; TensorFlow and PyTorch
- ❖ HPC applications
  - ▪ Parallel programming concepts
  - ▪ Multi core and multi-threading
  - ▪ Exploiting Parallelism
  - ▪ Shared and Distributed Memory
  - ▪ Issues in Parallelism and Improving Performance
  - ▪ Vector Parallelism

Faculty Conducting this Program

The program will be handled by thefaculty members from NIT-Warangal. In addition, academicians from IITs/NITs/IIITs are also expected to deliver the sessions as part of the course. Eminent professors are invited to deliver lectures in this

program. Speakers from industries are invited to handle hands-on sessions.

Registration Fee Particulars:

Faculty and Research Scholars	**Rs. 750/-**
Industry Participants	**Rs. 2250/-**

Participants need to pay the Registration Fee Online using the following details

Online Transfer Details
Account Name: Electronics & ICT Academy NITW
Account No: 62423775910
IFSC: SBIN0020149
Bank and Branch: State Bank of India, NIT (REC) Warangal

How to Apply

The interested participants are informed to register initially through the following link

https://docs.google.com/forms/d/1uXDbo4LwAnUxhstaJvD-RzAhVywn0Hjl1c2RjP94Eqc/edit

Selection Criteria

Selection will be done based on first-cum-first-serve basis and the confirmed candidates will be notified immediately. Maximum number of participants will be 100 (Hundred), additionally 10 participants from industry are allowed to participate. The list of selected participants will be notified through their personal e-mail ids. Assessment will be conducted at the end of the course. Participants will get e-certificate upon completion of the FDP/STC course.

Important Dates

Last date for submission of application	**16.06.2022**
Selection-list intimation at 5PM	**18.06.2022**

About NIT Warangal

National Institute of Technology, Warangal is the first among 17 RECs setup as joint venture of the Government of India and the state government. Over the years the college has established itself as a premier Institute imparting technical education of a very high standard leading to the B.Tech degrees in various branches of engineering, M.Tech. and Ph.D programmes in various specializations. All B. Tech and M. Tech programmes of NIT Warangal are NBA accredited.

There are 13 Departments offering eight undergraduate and 32 post-graduate programmes besides doctoral programmes. About

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5000 students across the country and about 500 international students study in the campus. It is a fully residential campus sprawling over 250 acres with excellent infra-structure, state of the art library, seminar halls, guest house and laboratory.

About JSS Academy of Technical Education, Bengaluru

JSS Academy of Technical Education (JSSATE) was established in the year 1997 at Bengaluru and is under the umbrella of JSS Mahavidyapeetha, Mysuru. JSSATE is the result of the vision of our President, "His Holiness Jagadguru Sri Shivarathri Deshikendra Mahaswamiji" to proactively participate in establishing a world class Institution for Technical Education. The Department of Electronics & Communication Engineering was established in the year 1997. The institution is affiliated to Visvesvaraya Technological University, Belagavi, Karnataka.

The UG-student strength of the department is 180 and PG student strength is 12 in VLSI and Embedded System and has University recognized research center which offers Ph.D program with Full – Time & Part – Time. The department encourage students to participate and involve in social causes, co-curricular & extra-Curricular activities which are

conducted under IEEE, ISTE, and IETE and AI &ML club. The Department has well qualified and highly experienced faculty members with rich research experience in allied domains. The department is currently headed by Dr. P M Shivakumaraswmy.

Vision

Enable the students to achieve academic excellence at par with premier institutes and to meet the requirements of industry and research in the field of Electronics and Communication Engineering.

Mission

- Orient the students to meet global challenges in the field of Electronics & Communications engineering with the state of the art facilities and excellent in teaching learning process.
- Impart knowledge in the merging areas through industry–institutes interaction, seminars, workshops and conferences.
- Provide strong foundation in core subjects to enable the students for continuous learning and research.
- Facilitate students for soft skills and professional ethics.

1. Name :

1. Designation :

2. Institution :

3. Email :

4. Mobile No :

5.

| Reference No : |
| Amount : |
| Bank : |
| Date : |

7. Address for Correspondence :

8. Educational Qualification :

9. Subjects taught so far:

10. No. of refresher courses/workshops attended :

11. Experience (in years) :

Teaching :   Research :   Industry :

12. Do you belong to SC/ST :   YES /NO

Declaration
The information provided is true to the best of my knowledge. If selected, I agree to abide by the rules and regulations of the FDP and shall attend the course for the entire duration. I also undertake the responsibility to inform the Coordinator in case, I am unable to attend the course.

Date   Signature of the Applicant

Course Objectives

Upon the completion of the STC, the participants/trainee can understand/Gain knowledge in solving the real-world problems with respect to AI, Data analytics and HPC.

Eligibility

The program is open to the faculty of Engineering Colleges, MCA, MBA Colleges and other allied disciplines in Telangana, Andhra Pradesh, Karnataka, Goa, Puducherry, Andaman and Nicobar Islands. Industry personnel working in the concerned/allied discipline can also attend. The Programme is open to all Faculty /Research Scholars / Industry Professionals from Degree/Polytechnic and other eligible students can register.

FDP Coordinator from NITW and JSSATE-Bengaluru

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FDP Convener from JSSATE-Bengaluru

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